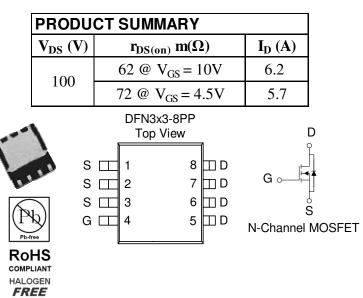
Analog Power

N-Channel 100-V (D-S) MOSFET

These miniature surface mount MOSFETs utilize a high cell density trench process to provide low $r_{DS(on)}$ and to ensure minimal power loss and heat dissipation. Typical applications are DC-DC converters and power management in portable and battery-powered products such as computers, printers, PCMCIA cards, cellular and cordless telephones.

- Low r_{DS(on)} provides higher efficiency and extends battery life
- Low thermal impedance copper leadframe DFN3x3-8PP saves board space
- Fast switching speed
- High performance trench technology



ABSOLUTE MAXIMUM RATINGS (T _A = 25 °C UNLESS OTHERWISE NOTED)						
Parameter	Symbol	Limit	Units			
Drain-Source Voltage			100	v		
Gate-Source Voltage			±20	v		
Continues Durin Consult ^a	T _A =25°C	T_	±6.2			
Continuous Drain Current ^a	$T_{A}=25^{\circ}C$ $T_{A}=70^{\circ}C$	тр	±5.1	А		
Pulsed Drain Current ^b	I _{DM}	±75				
Continuous Source Current (Diode Conduction) ^a	Is	16	А			
Power Dissipation ^a	T _A =25°C	P _D	3.5	w		
	$T_{A}=25^{\circ}C$ $T_{A}=70^{\circ}C$	I D	2	**		
Operating Junction and Storage Temperature Range		T _J , T _{stg}	-55 to 150	°C		

THERMAL RESISTANCE RATINGS						
Parameter	Symbol	Maximum	Units			
Maximum Junction-to-Case ^a	t <= 5 sec	$R_{\theta JC}$	25	°C/W		
Maximum Junction-to-Ambient ^a	t <= 5 sec	$R_{\theta JA}$	50	°C/W		

Notes

a. Surface Mounted on 1" x 1" FR4 Board.

b. Pulse width limited by maximum junction temperature

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AM7100N

Parameter	Symbol	Test Conditions	Limits			Unit	
Farameter	Symbol	Test Conditions	Min	Тур	Max	Umt	
Static							
Gate-Threshold Voltage	VGS(th)	$V_{DS} = V_{GS}$, $I_D = 250 \text{ uA}$	1			V	
Gate-Body Leakage	Igss	$V_{DS} = 0 V, V_{GS} = 20 V$			±100	nA	
Zero Gate Voltage Drain Current	IDSS	$V_{DS} = 80 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$			1	uA	
Zelo Gale voltage Dialii Current	IDSS	$V_{DS} = 80 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55^{\circ}\text{C}$			25	uA	
On-State Drain Current ^A	ID(on)	$V_{DS} = 5 V, V_{GS} = 10 V$	20			Α	
		$V_{GS} = 10 \text{ V}, \text{ ID} = 1 \text{ A}$			62	mΩ	
Drain-Source On-Resistance ^A	fDS(on)	$V_{GS} = 4.5 \text{ V}, I_D = 1 \text{ A}$			72		
Forward Tranconductance ^A	gís	$V_{DS} = 15 \text{ V}, I_D = 1 \text{ A}$		40		S	
Diode Forward Voltage	Vsd	Is = 1 A, VGs = 0 V		0.7		V	
Dynamic ^b							
Total Gate Charge	Qg	$V_{DS} = 15 V, V_{GS} = 4.5 V,$		13			
Gate-Source Charge	Qgs	VDS = 15 V, VGS = 4.5 V, ID = 9 A		3		nC	
Gate-Drain Charge	Qgd	ID = 9 A		6			
Input Capacitance	Ciss			1100		pF	
Output Capacitance	Coss	$V_{DS} = 15 \text{ V}, V_{GS} = 0 \text{ V},$ $f = 1 \text{ MHz}$		120			
Reverse Transfer Capacitance	Crss	I = IWIHZ		70			
Turn-On Delay Time	td(on)	$V_{DD} = 25 \text{ V}, \text{RL} = 25 \Omega \text{ , ID} = 1 \text{ A},$ $V_{GEN} = 10 \text{ V}$		11			
Rise Time	tr			12		nS	
Turn-Off Delay Time	td(off)			50		115	
Fall-Time	tf			30			

Notes

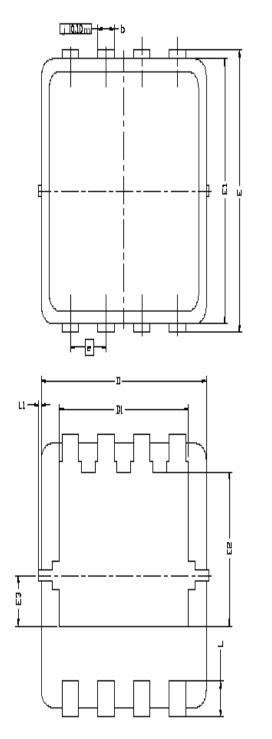
a. Pulse test: $PW \le 300$ us duty cycle $\le 2\%$.

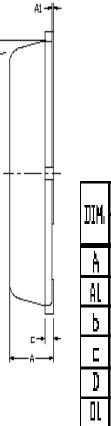
b. Guaranteed by design, not subject to production testing.

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TTM	HOLLIMETERS			INCHES			
DIM,	MIN		MAX	MIN	NDM	MAX	
Å	0,700	0,80	0,900	0.0276	0,0315	M334	
AL	D, DD		105	Q,QQQ		0,002	
b	0.24	0.3D	0.35	D.009	0.012	0.D14	
E	0,10	D.152	0,25	D.DD4	D.DD6	0.010	
D	3.00 BSC			0.118 BSC			
	2.35 BSC			0.093 BSC			
E	320 BSC			D126 BSC			
EL	3.00 BSC			0.118 BSC			
E3		1,75 BSC			0.069 BSC		
E3	(,	0.575 BSC			0.023 BSC		
P	¢	165 BS	~ L	0.026 BSC			
	0.30	0.40	D20	0.0118	0.0157	0.0197	
LL	D		Q.100	D		0.004	
81) ,	10 ,	12'	D .	1D,	15,	